Semaphores and their implementation

Reminder: The Semaphore concept

A semaphore is a shared integer variable. Its value is positive or 0 and it can only be accessed through the two operations \texttt{wait}(a) and \texttt{signal}(a), where \(a\) is an identifier representing the semaphore.

- \texttt{wait}(a) decrements \(a\) if \(a > 0\); if not, the process executing the operation \texttt{wait}(a) is suspended.

- \texttt{signal}(a) increments \(a\). The execution of \texttt{signal}(a) can have as result (possibly delayed) that a process waiting on the semaphore \(a\) resumes its execution. Executing a \texttt{wait}(a) or a \texttt{signal}(a) operation is done without any possible interaction (atomically).

Implementing Semaphores

- Semaphores are implemented in the system kernel.
  - The semaphore values are kept in a table stored in kernel memory. A semaphore is identified by a number corresponding to a position in this table.
  - There are system calls for creating or freeing semaphores, as well as for executing the \texttt{wait} and \texttt{signal} operations. These operations are executed in supervisor mode and hence atomically (interrupts are disabled in supervisor mode).

- In ULg03, to execute for instance a \texttt{wait} operation, the arguments of the system call, i.e., the semaphore number and a code \texttt{WAIT}, are placed on the stack. Assuming that the semaphore number is contained in \(r0\), this can be done as follows.

\begin{verbatim}
PUSH(r0) | 2nd argument
CMOVE(WAIT,r1) | 1st argument
PUSH(r1)
SVC() | system call
\end{verbatim}
The SVC handler

Before getting into the details of the handlers for semaphore operations, we will look at the general handler needed for the SVC instruction. The handler is reached through the following stub.

```
h_stub: ST(r0, User, r31) | save
   ST(r1, User+4, r31) | the registers
       ...               
   ST(r30, User+30*4, r31)
CMOVE(KStack, SP) | Load the system SP
BR(Svc_handler,LP) | Call the handler
LD(r31, User, r0) | restore
LD(r31, User+4, r1)
LD(r31, User+30*4, r30)
JMP(XP) | return to application
```

Note that the address saved in XP is that of the instruction following SVC.

The SVC handler: data structures

The data structures used by the kernel are the following.

```c
struct Mstate { int R0; ..., R30;} User; /* The saved state of the current process */
struct PTentry {short valid, resid; int PhysPage;}; /* An entry in the page table */
struct PTentry PageMap[262144]; /* The page table of the current process, 2^18 entries */
struct PD {struct Mstate state; int status, semwait; 
    struct PTentry PageMap[262144];} Proctbl[N]; /* The process table with page table, status, and the address of a blocking semaphore if any */
int sem[K]; /* The table of semaphore values */
int Cur; /* The index of the current process */
```

The SVC handler (continued)

- The program Svc_handler identifies the nature of the call and switches to the corresponding routine.
- One difficulty is that the arguments of SVC are on the stack of the process that has executed the system call, i.e. in its virtual space.
- These addresses must thus be translated before being used.

The SVC handler: translating virtual addresses

To have access to the stack of the process that has executed the instruction SVC, the system needs to translate into a physical address the address contained in r29 (SP). Indeed, even if the page table cache is up to date, address translation is not done by the hardware since we are in supervisor mode. This address translation can be done as follows.

```c
int Vmap(int Vaddress)
{
    int VpageNo, PageNo, Offset;
    VpageNo = Vaddress>>14; Offset = Vaddress & 16383;
    if (PageMap[VpageNo].valid == 0 || PageMap[VpageNo].resid == 0)
        Pagerror(VpageNo);
    PageNo = PageMap[VpageNo].PhysPage ;
    return((PageNo<<14) | Offset);
}
```
The SVC handler: general structure

The SVC handler extracts the code identifying the call and switches to the corresponding function.

Svc_handler()
{
  int code;
  code = *Vmap(User.R29-4)
  switch (code)
  {
    case WAIT : Wait_h(*Vmap(User.R29-8)); break;
    case SIGNAL : Signal_h(*Vmap(User.R29-8)); break;
    ....
  }
}

The addresses of the arguments of the system call are not computed exactly as in the case of a procedure call since, in the case of a system call, nothing is added to the stack of the calling process after the call.

The handlers Wait_h and Signal_h: first version

Wait_h(int semno)
{
  if (sem[semno] <= 0) {
    User.R30 = User.R30 - 4; /* SVC will be executed again */
    Proctbl[Cur].status = WAIT; Proctbl[Cur].semwait = semno;
    scheduler();
  } else
    sem[semno] = sem[semno] - 1;
}

When the value of the semaphore is not positive, the process is suspended; it will reexecute the SVC when it is reactivated.

Signal_h(int semno)
{
  for (i = 0; i<N; i++) {
    if (Proctbl[i].status == WAIT && Proctbl[i].semwait == semno)
      Proctbl[i].status = RUN;
  }
  sem[semno] = sem[semno] + 1;
}

The processes that are waiting on the semaphore are reactivated and the semaphore is incremented.

The system call is not reexecuted when a process leaves its wait state. Indeed, the handler for signal will take care of reactivating a waiting process if there is one, without the semaphore being incremented and decremented.

The handlers Wait_h and Signal_h: second version

Wait_h(int semno)
{
  if (sem[semno] <= 0) {
    User.R30 = User.R30 - 4; /* SVC will be executed again */
    Proctbl[Cur].status = WAIT; Proctbl[Cur].semwait = semno;
    scheduler();
  } else
    sem[semno] = sem[semno] - 1;
}

The system call is not reexecuted when a process leaves its wait state. Indeed, the handler for signal will take care of reactivating a waiting process if there is one, without the semaphore being incremented and decremented.
Signal_h(int semno) {
    int i, wokeup = 0;
    for (i = 0; i<N; i++) {
        if (Proctbl[i].status == WAIT && Proctbl[i].semwait == semno) {
            Proctbl[i].status = RUN;
            wokeup = 1; break;
        }
    }
    if (wokeup == 0) sem[semno] = sem[semno] + 1;
}

This implementation guarantees that, if only two processes use the semaphore, none will wait indefinitely.

A fair implementation for more than two processes will use a wait queue per semaphore for handling the waiting processes.

The instruction “Test and Clear”

This instruction copies a memory word to a register and sets it to 0.

TCLR(Ra, literal, Rc) : PC ← PC + 4
EA ← Reg[Ra] + SEXT(literal)  
Reg[Rc] ← Mem[EA] 
Mem[EA] ← 0

It’s opcode is chosen to be 0x04

.macro TCLR(Ra, Lit, Rc)  ENC_LIT(0b000100,Ra,Rc,Lit)

Implementing semaphores on a multiprocessor

- On a multiprocessor machine, execution in supervisor mode does not guarantee mutual exclusion since it can occur simultaneously on more than one processor.
- Another mechanism for implementing mutual exclusion is thus needed.
- Atomic memory reads and writes are not sufficient for a practical solution.
- One thus introduces a special instruction that can atomically read AND modify memory.

The microcode of TCLR

TCLR(Ra, Literal, Rc) (supervisor mode)

Opcode = 000100  IRQ = *  PC31 = 1

<table>
<thead>
<tr>
<th>Phase</th>
<th>FL</th>
<th>Latch</th>
<th>UC/D</th>
<th>ALU</th>
<th>LD</th>
<th>DR</th>
<th>SEL</th>
<th>PC+</th>
<th>SYR</th>
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<td>000000</td>
<td>0011</td>
<td>0011</td>
<td>0</td>
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<td>0</td>
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<td>000000</td>
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<td>0101</td>
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<td>0</td>
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<td>0011</td>
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<td>0</td>
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<td>0000</td>
<td>0101</td>
<td>0</td>
<td>0</td>
<td>INSTRREG ← DRAM</td>
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</table>
Mutual exclusion with TCLR

It is quite simple to implement mutual exclusion with TCLR.

wait: TCLR(r31, lock, r0)
BEQ(r0, wait)

... section critique ...

CMOVE(1, r0)
ST(r0, lock, r31)

This implementation uses an active wait and does not guarantee fairness among the waiting processes.

It is not used to implement mutual exclusion in general, but it is perfectly adequate for implementing semaphores on a multiprocessor machine.

The buffer problem: Synchronizing the consumer

To prevent the consumer from taking an element from an empty buffer, a semaphore that counts the number of elements in the buffer is used.

The "buffer" or "producer-consumer" problem

A Producer process sends a stream of information to a Consumer process. This stream of information goes through a buffer modeled as a table in shared memory accessed with the two functions (append and take).

```c
int in, out = 0;
int buf[N];

append(int v) { buf[in]= v; in = (in+1) % N; signal(n); }
int take() { int v; v = buf[out]; out = (out+1) % N; return v; }
```

As such these operations allow writing in a full buffer or reading from an empty one. they need to be synchronized.

The buffer problem: Synchronizing the consumer and the producer

A second semaphore, initialized to the number of empty slots in the buffer, is used to synchronize the producer.

```c
int in, out = 0;
int buf[N];
semaphore n = 0, e = N;

append(int v) { wait(e); buf[in]= v; wait(n); in = (in+1) % N; v = buf[out]; out = (out+1) % N; signal(n); }
int take() { int v; v = buf[out]; wait(n); out = (out+1) % N; signal(e); return v; }
```
The buffer problem: Mutual exclusion of the operations

If there are several producers and consumers, it is necessary to ensure that the buffer manipulation operations are performed in mutual exclusion. This can be done with an additional semaphore.

/* shared memory */
int in, out = 0; int buf[N];
semaphore n = 0, e = N, s = 1;

append(int v) {
  wait(e);
  wait(s);
  buf[in] = v;
in = (in+1) % N;
signal(s);
signal(n);
}

int take() {
  int v;
  wait(n);
  wait(s);
  v = buf[out];
out = (out+1) % N;
signal(s);
signal(e);
return v;
}

Binary semaphores

• A binary semaphore is a semaphore whose value can only be 0 or 1.

• Thus type of semaphore makes it possible to synchronize processes, but not to count.

• A general semaphore may nevertheless be simulated by a binary semaphore and a counter.

• Some problems can nevertheless be caused by the split between the semaphore and the counter.

The buffer with binary semaphores

/* shared memory */
int in = 0, out = 0, count = 0;
int buf[N]; int ewait = 0, nwait = 0;
semaphore n = 0, e = 0, s = 1;

append(int v) {
  wait(s);
  if (count == N) {
    ewait = 1; signal(s);
    wait(e);wait(s);ewait = 0;
  }
  buf[in] = v; in = (in+1) % N;
count = count+1;
  if (count == 1) signal(n);
signal(s);
}

int take() {
  int v;
  if (count == 0) {
    nwait = 1; signal(s);
    wait(n);wait(s);nwait = 0;
  }
  v = buf[out]; out = (out+1) % N;
count = count-1;
  if (nwait > 0) signal(n);
signal(s);
}

This solution is not correct since it is possible to execute append; take; take. The problem is that the presence of an element is signaled twice: by setting count to 1 and by the operation signal(n).
• To avoid signaling twice, \( \text{signal(n)} \) or \( \text{signal(e)} \) are only executed if a process is waiting.

• One can imagine extending this solution to several producers and consumers by counting the number of waiting processes.

• However, one must for example avoid a situation in which a second consumer overtakes a consumer that has just been allowed to proceed (has been released) and consumes the element intended for this consumer.

• This can be done by transferring mutual exclusion to the process that has been released.

Conclusions on semaphores

• Semaphores are an excellent mechanism for handling processes that have to be suspended (put in a wait state).

• When suspending processes is combined with manipulating a counter, semaphores are perfectly well adapted.

• Attempting to use binary semaphores shows that difficulties can occur when waiting implemented with semaphores is combined with manipulating another data structure.

• It would be useful to have a systematic approach for handling this type of problem.

The buffer with binary semaphores:
Solution for several producers and consumers

```c
/* shared memory */
int in = 0, out = 0, count = 0;
int buf[N]; int ewait = 0, nwait = 0;
semaphore n = 0, e = 0, s = 1;

append(int v)
{ wait(s);
  if (count == N) {
    ewait++; signal(s); wait(e);
    ewait--;
  }
  buf[in] = v; in = (in+1)%N;
  count = count+1;
  if (nwait > 0) signal(n);
  else signal(s);
}

int take()
{ int v;
  wait(s);
  if (count == 0) {
    nwait++; signal(s); wait(n);
    nwait--;
  }
  v = buf[out]; out = (out+1)%N;
  count = count-1;
  if (ewait > 0) signal(e);
  else signal(s);
  return v;
}
```